

28. A module for processing signals comprising:

A1  
at least a top base ring, intermediate base rings, and a bottom base ring, each ring having at least four nodes, the nodes of the top ring in communication with corresponding nodes of an immediately adjacent intermediate ring, the nodes of each intermediate base ring in communication with corresponding nodes of an immediately adjacent intermediate ring, the nodes of the bottom ring in communication with corresponding nodes of an immediately adjacent intermediate ring, and the nodes of the top ring in communication with opposing nodes of the bottom ring.

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